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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	Unknown 10/731,691
Filing Date	Even Date Herewith
First Named Inventor	Janik, Kenneth
Group Art Unit	Unknown 2181
Examiner Name	Donaghy, Larry W. TREAT

Sheet 1 of 3

Attorney Docket No: 884.002US6

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
WMT	US-4,658,355	04/01/1987	Hatakeyama, et al.	712	4	
	US-5,363,495	11/08/1994	Fry, R. E., et al.	395	375	08/26/1991
	US-5,428,807	06/01/1995	McKeen, et al.	712	216	
	US-5,497,499	03/05/1996	Garg, S., et al.	395	800	03/29/1994
	US-5,572,690	11/05/1996	Molnar, C. E., et al.	395	376	06/06/1995
	US-5,600,848	02/01/1997	Sproull, et al.	712	42	
	US-5,682,493	10/01/1997	Yung, et al.	712	217	
	US-5,699,460	12/01/1997	Kopet, et al.	382	307	
	US-5,704,054	12/01/1997	Bhattacharya	712	212	
	US-5,748,936	05/01/1998	Karp, et al.	712	218	
	US-5,758,139	05/01/1998	Sutherland, et al.	713	600	
	US-5,805,838	09/01/1998	Sutherland, et al.	710	112	
	US-5,838,939	11/01/1998	Sutherland	712	200	
	US-5,913,059	06/15/1999	Torii, S.	395	674	07/25/1997
	US-5,943,491	08/01/1999	Sutherland, et al.	709	400	
	US-5,944,816	08/31/1999	Dutton, D. J., et al.	712	215	05/17/1996
	US-5,974,524	10/01/1999	Cheong, et al.	712	23	
	US-5,987,588	11/16/1999	Popescu, V., et al.	712	23	08/28/1998
	US-6,065,109	05/16/2000	Coates, W. S.	712	201	02/05/1997
	US-6,085,316	07/01/2000	Sutherland, et al.	712	245	
	US-6,185,668	02/01/2001	Arya	712	23	
WMT	US-6,205,538	03/01/2001	Yung	712	201	
WMT	US-6,381,692	04/01/2002	Martin, et al.	712	244	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
WMT		BHANDARKAR, D., et al., "Performance Characterization of the Pentium Pro Processor", Proceedings of the Third International Symposium on High-Performance Computer Architecture, San Antonio, TX, (Feb. 1-5, 1997), pp. 288-297	
WMT		BURGER, D., "The SimpleScalar Tool Set, Version 2.0", University of Wisconsin-Madison Computer Sciences Department Technical Report #1342, (June 1997), pp. 1-21	
WMT		CARLSON, R., et al., "VRP Simulator", http://www.ece.orst.edu/~silu/cfpp/vrpsim/docs/vrpsim.html , (April 1996), 12 p.	

EXAMINER

W. TREAT

DATE CONSIDERED 5/18/06

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Applicant Number	Unknown 10/731,691
Filing Date	Even Date Herewith
First Named Inventor	Janik, Kenneth
Group Art Unit	Unknown 2161
Examiner Name	Donaghue, Larry W. TREAT

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Attorney Docket No: 884.002US6

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Went		CHILDERS, B. R., et al., "A Design Environment for Counterflow Pipeline Synthesis", <u>ACM Sigplan Workshop Proceedings on Languages, Compilers, and Tools for Embedded Systems</u> , Montreal, Canada, (June 19-20, 1998), pp. 223-234	
		CHILDERS, B. R., et al., "Application-Specific Pipelines for Exploiting Instruction-Level Parallelism", <u>University of Virginia Computer Science Technical Report No. CS-98-14</u> , (May 1, 1998), 10 p.	
		CHILDERS, B. R., et al., "Automatic Counterflow Pipeline Synthesis", <u>University of Virginia Computer Science Technical Report No. CS-98-01</u> , (Jan. 1998), 6 p.	
		CHILDERS, B. R., et al., "Synthesis of Application-Specific Counterflow Pipelines", <u>Department of Computer Science Slides of the Workshop on the Interaction between Compilers and Computer Architecture</u> , San Jose, CA, (Feb. 4, 1996), 5 p.	
		JANIK, KENNETH J., et al., "Advances of the Counterflow Pipeline Microarchitecture", <u>IEEE Computer Soc. Press - Proceedings of the Third International Symposium on High-Performance Computer Architecture</u> , (1997), 7 p.	
		JANIK, K. J., et al., "Synchronous Implementation of a Counterflow Pipeline Processor", <u>Proceedings of the 1996 International Symposium on Circuits and Systems</u> , 4, (May 12-15, 1996), 6 p.	
		JONES, M. D., "A New Approach to Microprocessors", http://lal.cs.byu.edu/people/jones/latex/sproull.html/sproull.html.html , (1994), pp. 1-17	
		JONES, MICHAEL D., "Future Computer Plumbing", <u>Insight</u> , 10 (1), (1994), pp. 50-61	
		JOSEPHS, M. B., et al., "Formal design of an asynchronous DSP counterflow pipeline: a case study in Handshake Algebra", <u>Proceedings of the International Symposium on Advanced Research in Asynchronous Circuits and Systems</u> , Salt Lake City, Utah, (Nov. 3-5, 1994), pp. 206-215	
		KORVER, "Asynchronous implementation of the SCPP-A counterflow pipelined processor", pp. 287-294	
		LO, J. L., et al., "Converting Thread-Level Parallelism to Instruction-Level Parallelism via Simultaneous Multithreading", <u>ACM Transactions on Computer Systems</u> , 15 (3), (Aug. 1997), pp. 322-354	
		MILLER, et al., "Non-Stalling Counterflow Architecture", (Feb., 1998), 334-341	
		SMITH, J. E., et al., "The Microarchitecture of Superscalar Processors", <u>Proceedings of the IEEE</u> , 83 (12), (Dec. 1995), pp. 1609-1624	
Went		SPROULL, ROBERT F., et al., "The Counterflow Pipeline Processor Architecture", <u>IEEE Design & Test of Computers</u> , Vol. 11, No. 5, (Fall 1994), pp. 48-59	

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W. TREAT

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5/15/2006

Substitute Disclosure Statement Form (PTO-1449)

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First Named Inventor	Janik, Kenneth
Group Art Unit	Unknown 2181
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Sheet 3 of 3

Attorney Docket No: 884.002US6

WMT		WERNER, "Asynchronous Processor Survey", IEEE, (Nov. 1997), 67-76	
WMT		WERNER, et al., "Counterflow Pipeline Based Dynamic Instruction Scheduling", IEEE, 69-79	
WMT		YAKOVLEV, A. , "Designing Control Logic for Counterflow Pipeline Processor Using Petri Nets", University of Newcastle upon Tyne Technical Report No. 522, (May 3, 1995), pp. 1-24	

EXAMINER

W. TREAT

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